



# Books and papers

Here are the books and papers I have published since 2005, all freely downloadable as PDF files from the most recent to the older.

## In English

- **How to compare logical qubits?, a technical paper of 45 pages (March 2026).**

**How to compare logical qubits?**  
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(Draft, March 10, 2026)

As the transition from NISQ (noisy intermediate-scale quantum computing) to FTQC (fault-tolerant quantum computing) gains momentum, quantum scientists and industry vendors face a complexity challenge: not only to deliver what's in their plans, budgets and roadmaps but also on how to communicate about it. Many underlying concepts and figures of merit deserve to be explained given all logical qubits are not born equal and experiments are different from one to the other. This paper is structured as a tutorial and review paper which describes how quantum error correction, logical qubits, and fault-tolerance operate from a system engineering standpoint. It also helps understand what quantum computing industry vendors are planning and differentiating. It draws the links between concepts, techniques, figures of merit, software and hardware components. It is of particular interest for quantum scientist developers and engineers who need to connect the dots between classical use requirements and FTQC systems present or future capabilities. While vendors are driving the development and implementation of error correction and fault-tolerance engines, we outline that their critical path lies in the improvement of the scaling of their hardware, physical qubit operation fidelities, and connectivity.

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**I. Introduction**

While several "quantum advantages" have been claimed with NISQ quantum computers (noisy intermediate-scale quantum computing) in several quantum physics simulation experiments [1–4], the consensus with quantum computer science academics and industry vendors is that industry utility quantum advantage will necessitate the use of FTQC (fault-tolerant quantum computing) [5].

In most cases, NISQ algorithms face various limitations, particularly when being based on variational circuits, which are plagued by scaling problems like barren plateaus as the number of qubits increases [6]. These circuits can exploit techniques like quantum error mitigation [7] and improvements in physical qubit fidelities. It can currently extend their reach to about 1,500 physical gates and 100 to 200 physical qubits. But industry-grade algorithms require between a million gates ( $10^6$ ) and a billion gates ( $10^9$ ), or at least beyond  $10^6$  for some resource estimations in computational fluid dynamics and complex chemical simulations [8, 11]. Only this capability will enable the execution of large gate-based quantum circuits supporting a wealth of quantum algorithms for quantum chemistry simulations, solving various linear equations, up to, potentially, reaching the realm of quantum machine learning and combinatorial optimization [12].

The quest for the creation of utility-grade FTQC systems drives a mobilization of academics and industry vendors. Quantum computing industry vendor published roadmaps span from 5 to 10 years. All face a complexity challenge, particularly due to the difficulty to scale the number of physical qubits while maintaining and even improving their operation fidelities. The field of quantum error correction (QEC) and fault-tolerant quantum computing (FTQC) is both burgeoning and cryptic. Most related scientific papers are hard to grasp by quantum software developers. The quantum computing community faces a double complexity challenge for the development of FTQC systems and on how to create bridges with the quantum software community.

Qubit platforms and industry vendors are at different stages in maturity. Some have even not yet reached a minimum level to support basic NISQ applications with at least a couple dozen physical qubits. They may publish theoretical results on various hardware assumptions. Other vendors may conduct impressive experiments but their results are made of various components which are not always assembled in an end-to-end fashion. A last small category of vendors directly target FTQC systems with shipping the NISQ route for various technical reasons.

On the end-user side, software developers can adopt relatively simple figures of merit like the reported circuit size in existing fault FTQC systems, estimated computing time and FTQC system costs. These figures of merit are rarely accurate when industry vendors describe their logical qubits. There are multiple technical reasons for this as we will see. One is that the field of QEC and FTQC is incredibly complex. We propose here to create a bridge between the quantum software and QEC-FTQC communities.

This material paper is here to clarify the situation and help quantum software developers, quantum computing engineers and others to connect the dots between the QEC-FTQC academic and industry landscape and their algorithmic needs. It is organized as follows:

**Section I** clarifies the difference between quantum error correction and fault-tolerant quantum computing and summarizes in a large table the key components of QEC and FTQC. It highlights what computer information science specialists may look for in industry vendor achievements as well as roadmaps, and how to find it.

**Section II** describes how quantum error correction works on a logical qubit memory, what types of errors are corrected and how. It is focused on surface, color, and QLDPC codes.

**Section III** elaborates on the various notions of logical qubits and some related scaling aspects.

**Section IV** looks at the key features of fault-tolerance and how they are implemented, particularly for the realization of logical quantum gates. It helps quantify the space, time and energetic overheads of QEC-FTQC.

**Section V** provides an overview of the current industry vendor achievements across multiple qubit modalities with a mix of experiments, theoretical work, and blueprints. It comes from the vendors themselves, and from the academic labs which are working with them.

Then, a **glossary** defines the key technical terms mentioned in this paper.

**A. What is the difference between QEC and FTQC?**

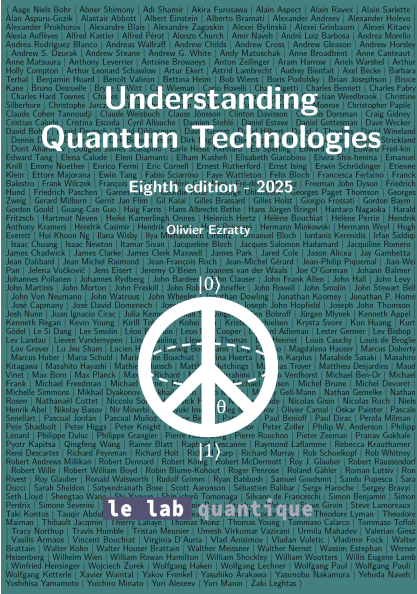
Let's first define what are quantum error correction (QEC) and fault-tolerant quantum computing (FTQC). There's a blurry line between both depending on the definition.

Quantum error correction is associated with the act to correct the data stored in a quantum memory made of logical qubits, also "data at rest", corresponding to the qubits that are idle for some time during a circuit execution (see it represented in Figure IV E). It fights against decoherence and extends the qubit information lifetime. That's what Google did with its Willow chip experiment in 2024. In many cases, multiples logical qubits are corrected simultaneously [13, 14].

Fault-tolerance is about extending QEC using multiple features with the end goal being to enable the successful execution of a quantum circuit with logical operations error rates compatible with its size.

The first definition of FTQC was crafted by Peter Shor in a seminal 1997 paper [15]. It was the ability to construct a polynomial-sized quantum circuit that can successfully run on a quantum computer despite the presence of decoherence and gate inaccuracies. It comprised multiple elements: the definition of new error tolerance threshold of  $O(1/\log^2)$  inaccuracies and decoherence per gate for a computation with  $n$  gates, or being a constant, the use of error correction codes without decoding the data itself, using more gates to compute directly on encoded qubits and correcting errors as they occur, correcting decoherence errors and qubit control inaccuracies errors.

- **Understanding Quantum Technologies 2025, 8th edition, published in September 2025, 1,524 pages. A short 38-pages version is also available.**



• **Quantum computing and artificial intelligence: status and perspectives** by Giovanni Acampora, Andris Ambainis, Natalia Ares, Leonardo Banchi, Pallavi Bhardwaj, Daniele Binosi, G. Andrew D. Briggs, Tommaso Calarco, Vedran Dunjko, Jens Eisert, Olivier Ezratty, Paul Erker, Federico Fedele, Elies Gil-Fuster, Martin Gärtnert, Mats Granath, Markus Heyl, Iordanis Kerenidis, Matthias Klusch, Anton Frisk Kockum, Richard Kueng, Mario Krenn, Jörg Lässig, Antonio Macaluso, Sabrina Maniscalco, Florian Marquardt, Kristel Michielsen, Gorka Muñoz-Gil, Daniel Müssig, Hendrik Poulsen Nautrup, Evert van Nieuwenburg, Roman Orus, Jörg Schmiedmayer, Markus Schmitt, Philipp Slusallek, Filippo Vicentini, Christof Weitenberg, and Frank K. Wilhelm, arXiv, May 2025 (32 pages). I coordinated the redaction of this paper from June 2024 to June 2025 on behalf of the European Quantum Flagship.

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**Quantum computing and artificial intelligence: status and perspectives**

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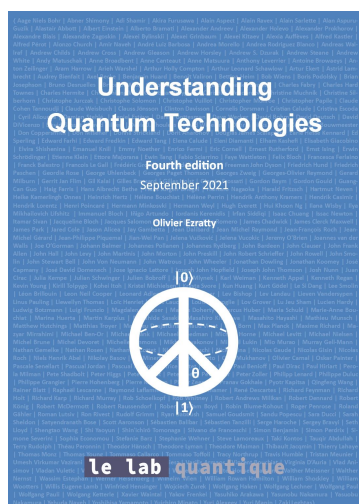
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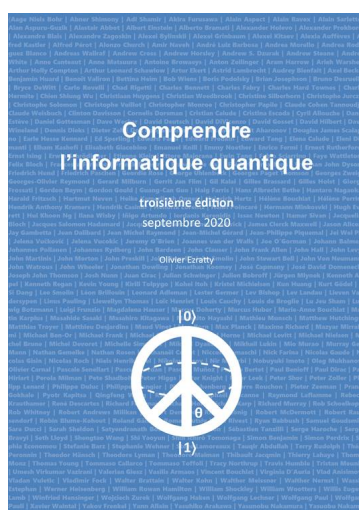






## In French

- L'ebook **Comprendre l'informatique quantique**, troisième édition, publiée en septembre 2020, 682 pages, les deux premières datant de septembre 2018 et septembre 2019. C'est un condensé unique et complet qui fait un tour d'horizon très large des enjeux scientifiques, technologiques, industriels, géopolitiques et sociétaux de l'informatique quantique.



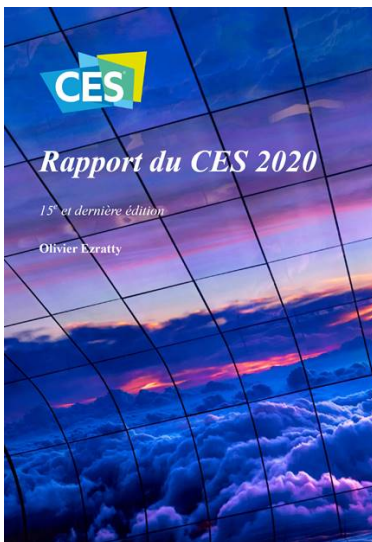
- L'ebook **Les usages de l'intelligence artificielle 2021**, en février 2021, 742 pages, une grosse mise à jour des éditions de 2017, 2018 et 2019.



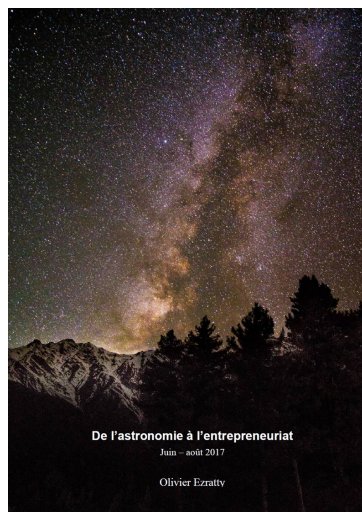
- **Le Guide des Startups 2019**, 23e édition, en avril 2019, 548 pages. C'est la dernière édition en date de 2022.



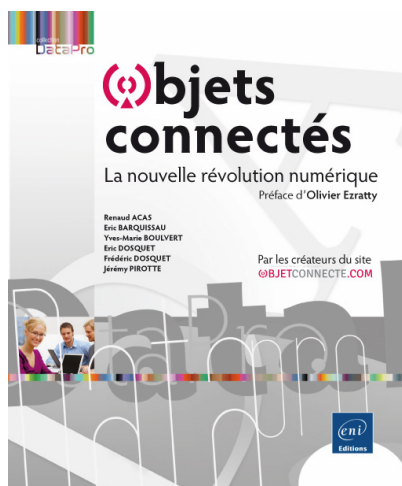
- **Le Rapport du CES 2020**, publié en janvier 2020, de 366 pages, le dernier d'une série lancée en 2006.



- Une compilation de 12 articles sur **l'astronomie et l'entrepreneuriat** publiée en août 2017 sous forme d'un ebook. Elle est focalisée sur la découverte des instruments d'observation de l'Univers, les télescopes spatiaux et terrestres ainsi que les radiotélescopes, puis sur les entrepreneurs qui s'intéressent à la conquête spatiale.



- J'ai rédigé la préface de "Objets Connecté – La nouvelle révolution numérique", un livre paru en février 2016 aux éditions ENI et écrit par Renaud Acas, Eric Barquisseau, Yves-Marie Boulvert, Frédéric Dosquet, Eric Dosquet et Jérémy Pirotte.



- Le Rapport du CES 2017, publié en janvier 2017. 396 pages, dans la lignée des rapports précédents.

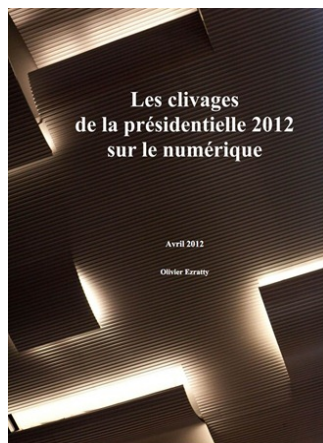


- "Tout tout tout sur la high-tech", publié aux éditions Kawa. Il s'agit de la version "livre" du Rapport du

CES 2013.



- Une contribution sur le thème des blogs et celui des réseaux sociaux dans le **Dictionnaire Politique de l'Internet et du Numérique**, un ouvrage collectif paru en 2009 et diffusé par La Tribune.
- Le **Rapport du CES 2013**, publié en janvier 2013. 272 pages.
- Le **Rapport du CES 2012**, publié en janvier 2012. 244 pages.
- Le **Rapport du CES 2011**, publié en janvier 2011. 246 pages.
- Le **Rapport du CES 2010**, publié en janvier 2010. 204 pages.
- Le **Rapport du CES 2009**, publié en janvier 2009. 192 pages.
- Le **Rapport du CES 2008**, publié en janvier 2008. Ce document de 178 pages dresse un panorama très complet des technologies présentées au CES et lancées pendant l'année 2007.
- Le **Rapport du CES 2007**, publié en janvier 2007. Ce document de 164 pages avec 435 illustrations fait le tour des nouveautés présentées au CES et de l'année 2006.
- Le **Rapport du CES 2006**, publié en février/mars 2006 qui décrit les grandes tendances de ce salon. J'y décris également la présence française sur ce salon et les leçons que l'on peut en tirer.
- Le livre blanc sur **Les opportunités de la télévision numérique** qui fait un point sur ce secteur d'activité et particulier sur le développement de l'IPTV. Il met en évidence les bouleversements en cours de la chaîne de valeur de ce secteur et propose quelques pistes d'actions pour l'industrie française.
- **Les clivages de la présidentielle 2012 sur le numérique**, un recueil d'une série de six articles au format PDF sur la manière dont les candidats à la présidentielle 2012 ont traité le thème du numérique. Le document traite également des propositions des professionnels du secteur et fournit quelques pistes de solutions à différents problèmes notamment au sujet de l'entrepreneuriat et de l'enseignement supérieur.



- Un **panorama des logiciels photo** qui reprend et complète une série de sept articles publiés en septembre 2008 sur les logiciels et services en ligne dédiés au traitement de la photo numérique.



- Un ensemble de propositions pour les Assises du Numérique “**Développer l’entrepreneuriat et faire réussir les startups dans les TICs**” publié en juillet 2008. C’est un document conçu de manière collective avec un grand nombre de contributeurs, cités dans la couverture.
- Un essai comparant **Google et Microsoft**, dans leurs stratégies de croissance, les effets de leur taille, leur relation au marché et à leur écosystème.
- Un **Trop d’Etat – Oui mais où ça?**, un essai publié en mars 2007 analysant les dépenses du secteur public dans son ensemble et tentant d’identifier où des économies ou redéploiements pourraient être réalisés.
- La coordination d’un numéro spécial “Opportunités de l’industries informatique en France” de la revue Centraliens (des anciens élèves de l’Ecole Centrale Paris) paru en juin 2006 et dont l’un des articles est disponible sur ce blog: **l’interview de Bernard Liautaud** de Business Objects.
- Une série d’articles sur le thème “Décrypter Microsoft” publiés dans la revue **DecisionMakers IT**, le premier étant dédié aux **programmes partenaires** de l’éditeur.
- Un **comparatif de lecteurs RSS** sous forme de tableau commenté associé à **ce post**.
- Un **Compte-rendu d’installation de Windows Vista**, datant de novembre 2006.
- Une étude de cas sur “**Le défi de l’innovation dans Windows**“, publiée à l’occasion d’une présentation

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réalisée aux Entretiens de la Technologie de Louis le Grand organisés par l'Institut de l'Entreprise en août 2005. Je l'ai faite pour le compte de Microsoft France.